

FIG. 1

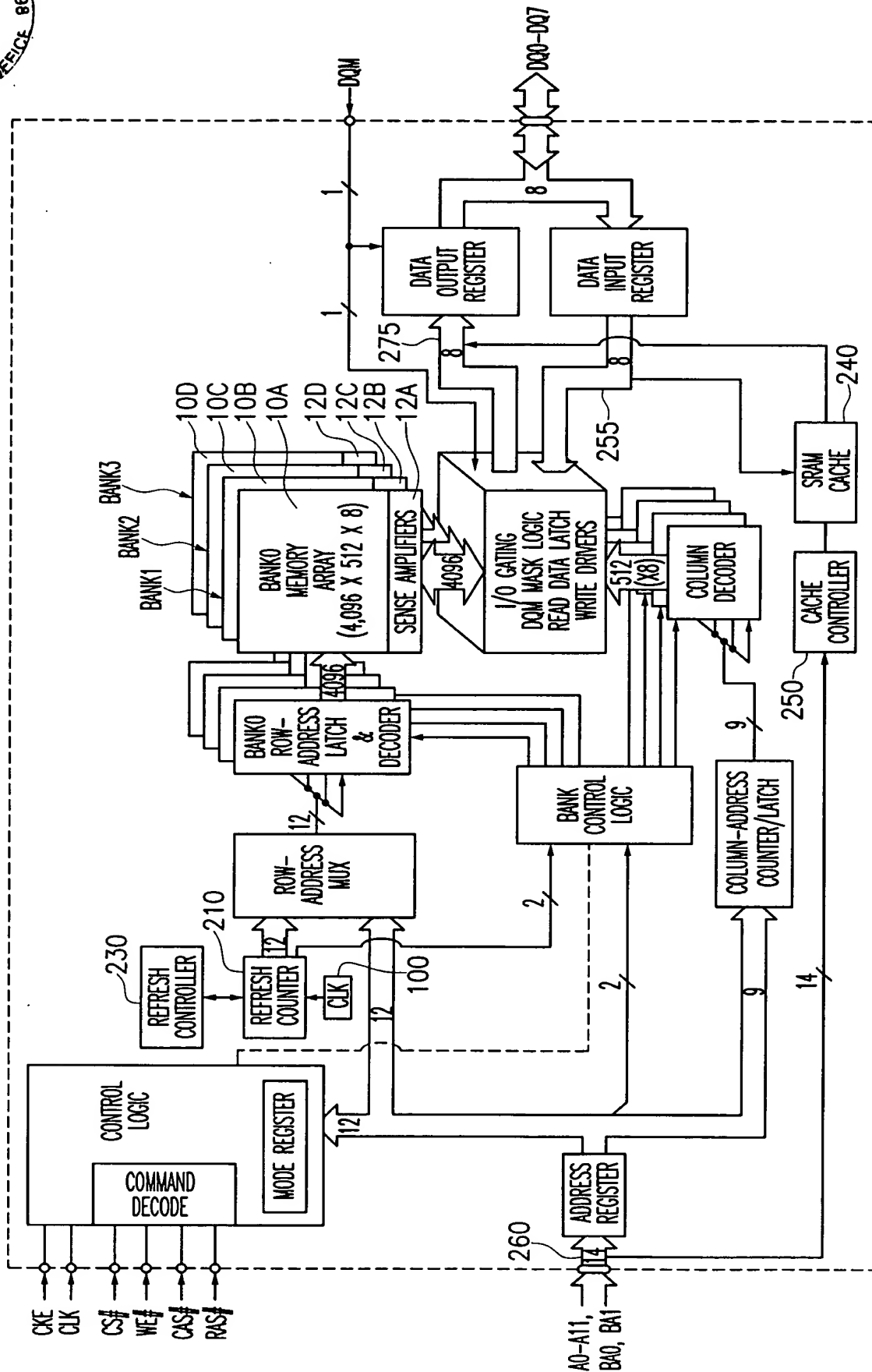
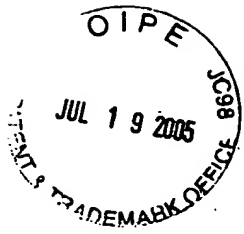


FIG. 2

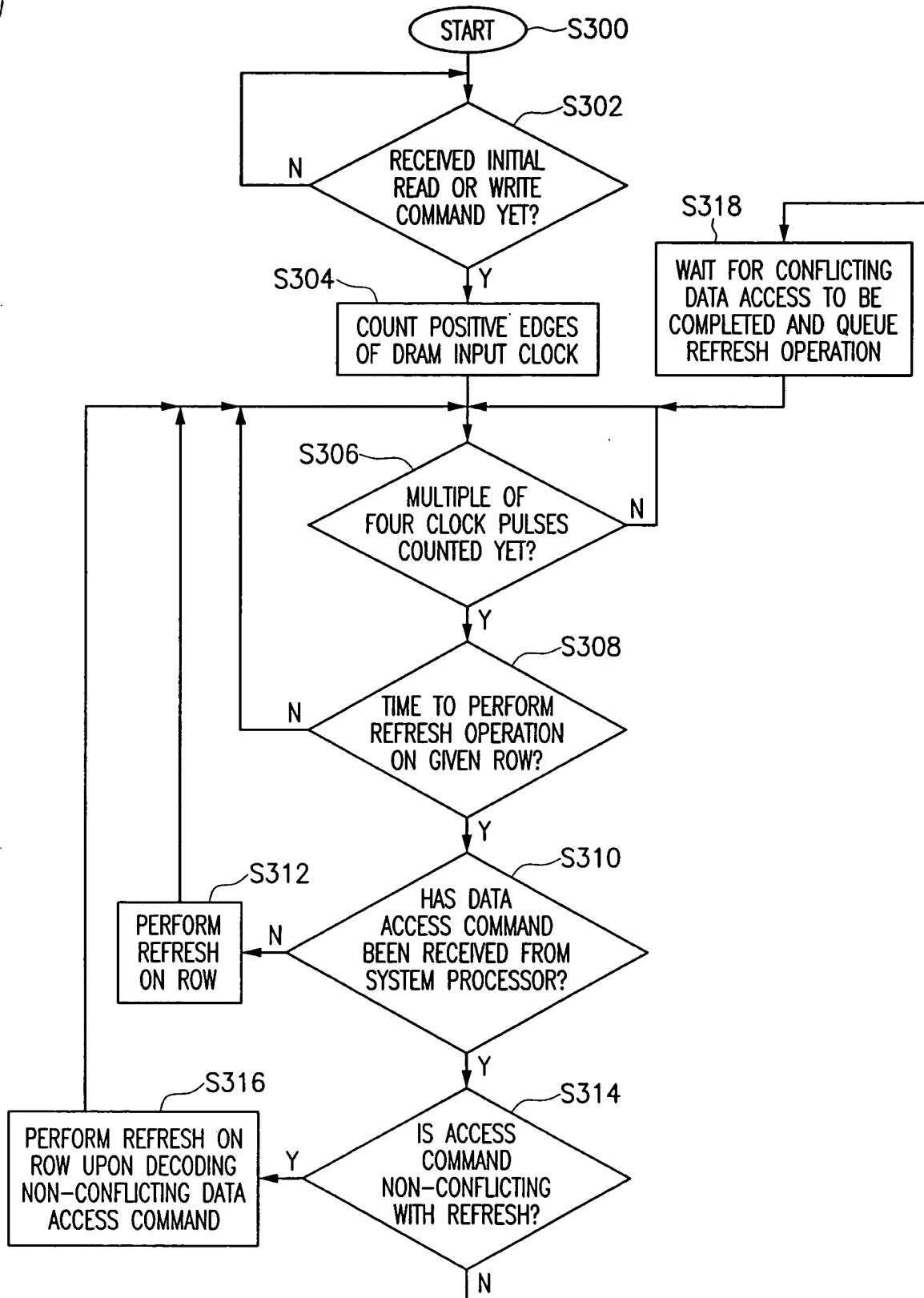


FIG. 3



READ-READ Timing Diagram

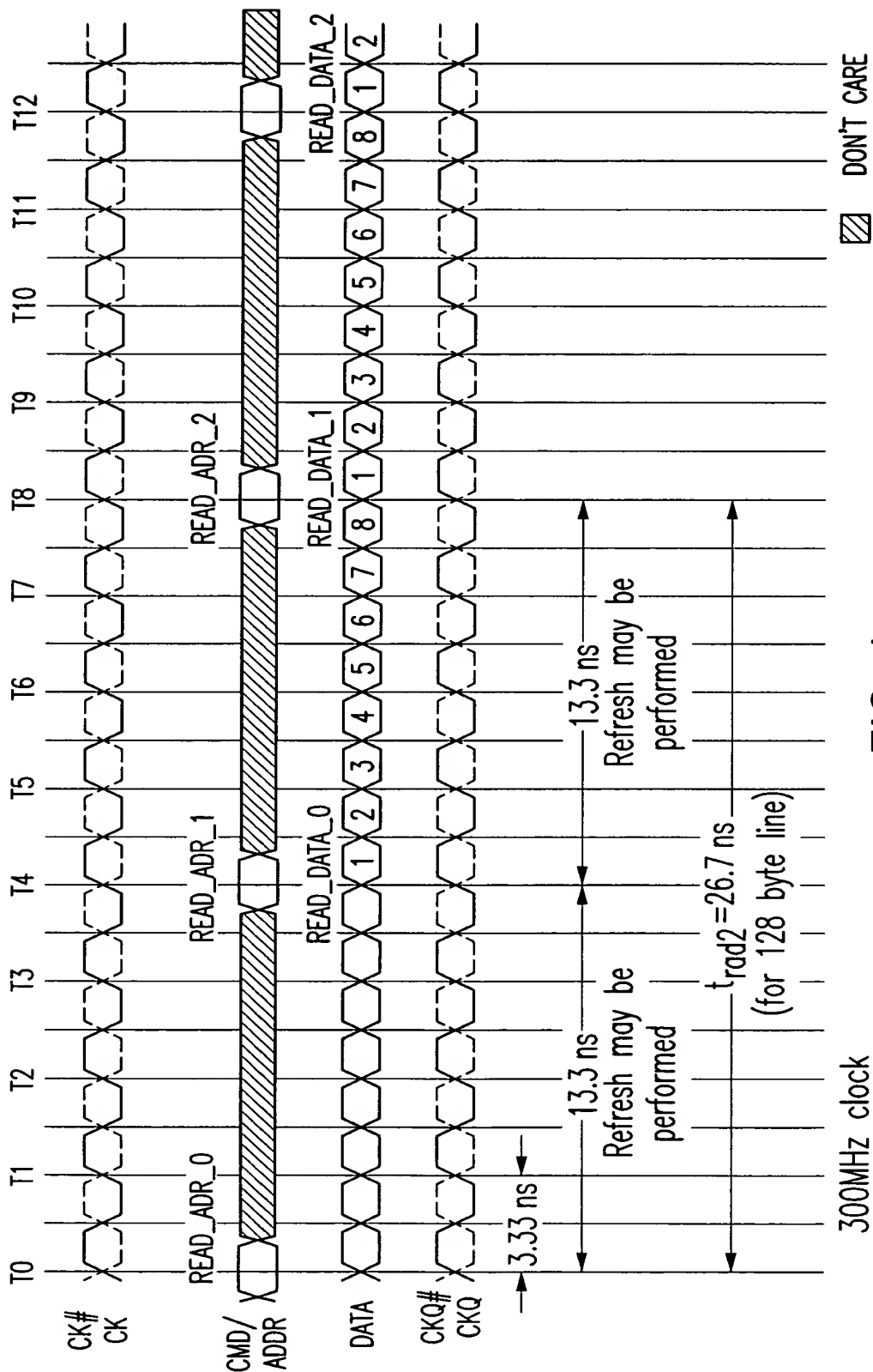


FIG. 4



WRITE-WRITE Timing Diagram

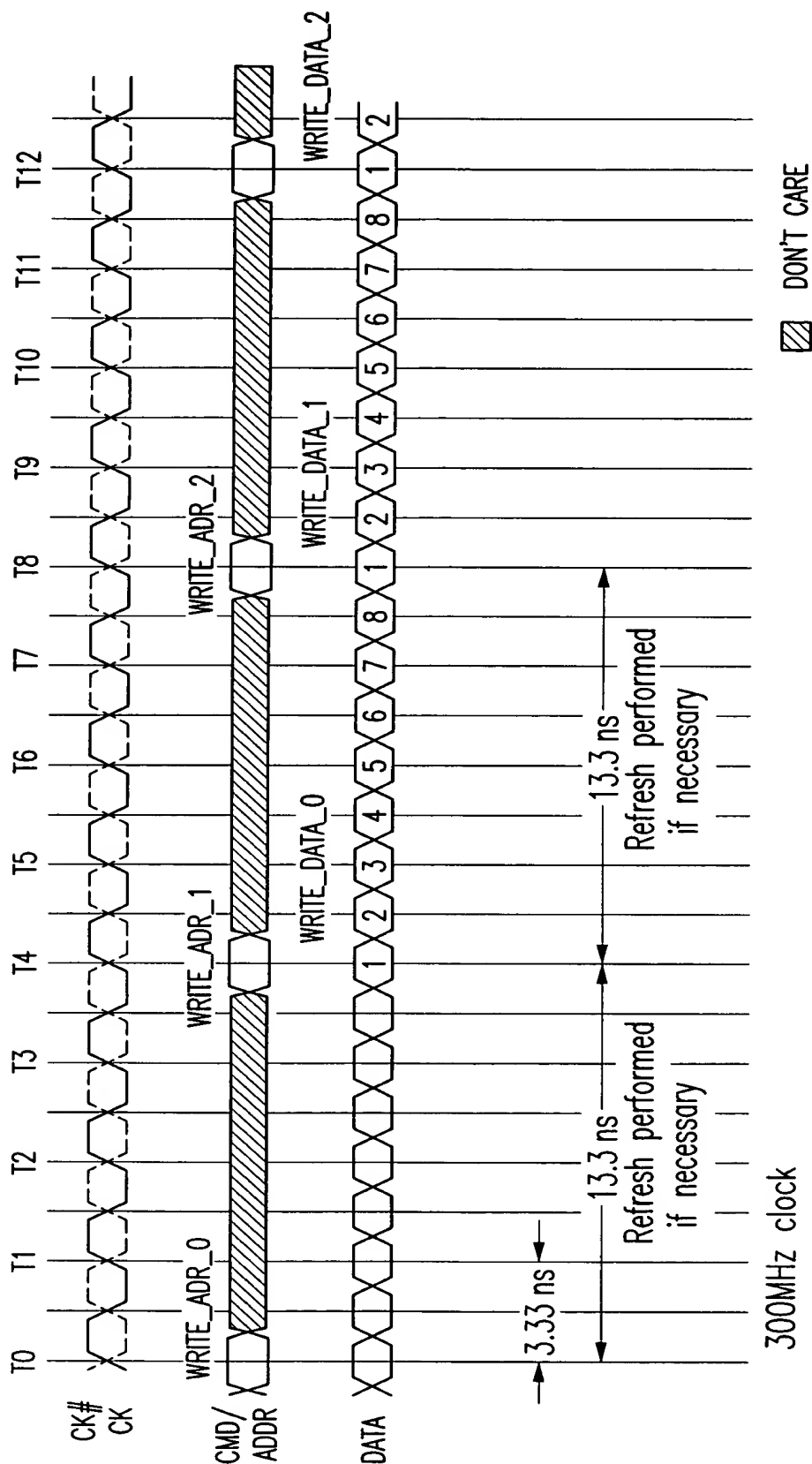


FIG. 5

300MHz clock

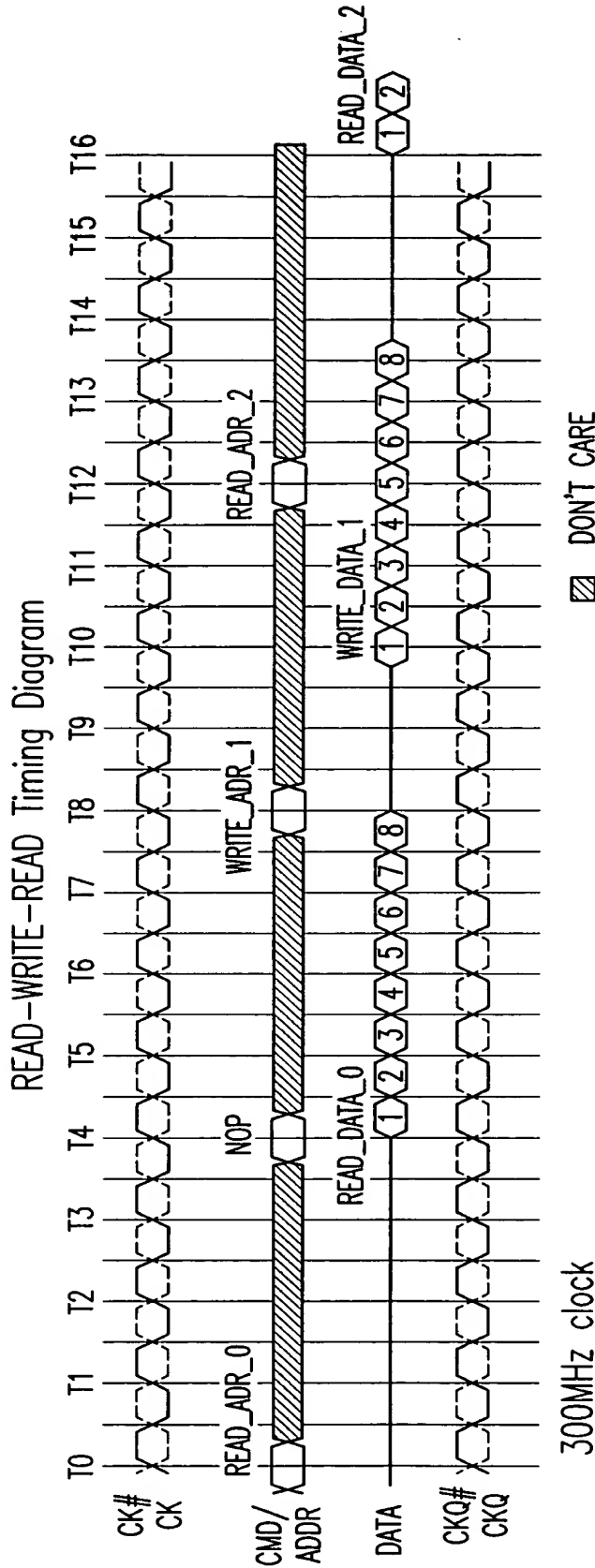


FIG. 6

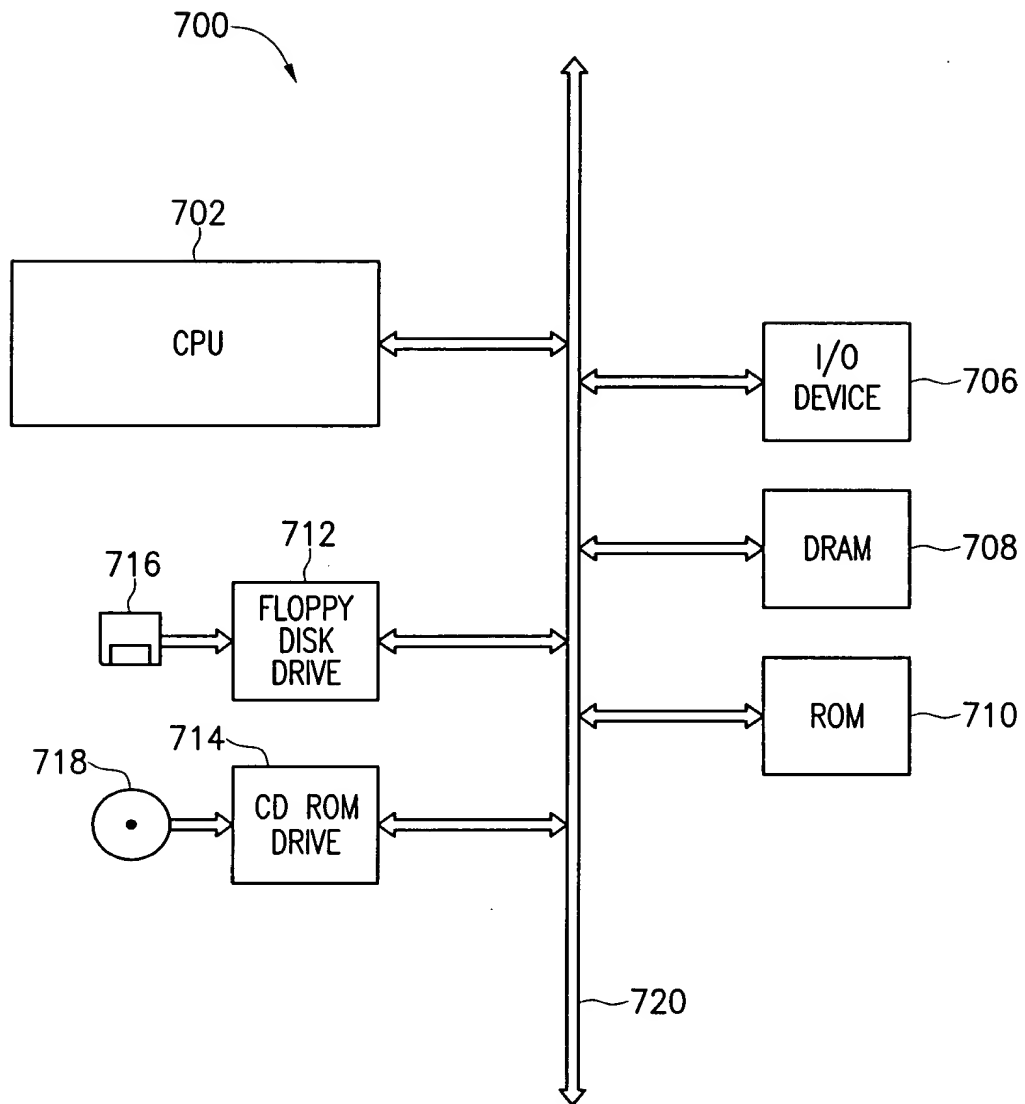


FIG. 7